

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

1c571 U.S. PTO  
09/598926



**INFORMATION DISCLOSURE  
STATEMENT**

Docket Number:  
**2885/31**

Application Number  
**To be assigned**

Filing Date  
**Herewith**

Examiner  
**Not yet assigned**

Art Unit  
**Not yet assigned**


Invention Title  
**METHOD OF REPAIRING INTEGRATED CIRCUITS**

Inventor(s)  
**VORBACH et al.**

Address to:  
Assistant Commissioner for Patents  
Washington D.C. 20231

1. In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorneys for Applicants hereby bring the following references to the attention of the Examiner. These references are listed on the attached modified PTO Form No. 1449. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.
2. A copy of each patent, publication or other information listed on the modified PTO form 1449 is enclosed, except as otherwise indicated..

Dated: *21 June 2000*

By:   
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